ACM TRETS Best Paper Awards

The ACM Transactions on Reconfigurable Technology and Systems (TRETS) has awarded Best Paper Awards to the following papers:

2026

Understanding the Potential of FPGA-based Spatial Acceleration for Large Language Model Inference
Hongzheng Chen, Jiahao Zhang, Yixiao Du, Shaojie Xiang, Zichao Yue, Niansong Zhang, Yaohui Cai, and Zhiru Zhang

Honorable Mentions:

On the Malicious Potential of Xilinx's Internal Configuration Access Port (ICAP)
Nils Albartus, Maik Ender, Jan-Niklas Möller, Marc Fyrbiak, Christof Paar, and Russell Tessier

HLPerf: Demystifying the Performance of HLS-based Graph Neural Networks with Dataflow Architectures
Chenfeng Zhao, Clayton Faber, Roger Chamberlain, and Xuan Zhang

2020

VTR 8: High-performance CAD and Customizable FPGA Architecture Modelling
Kevin E. Murray, Oleg Petelin, Sheng Zhong, Jia Min Wang, Mohamed Eldafrawy, Jean-Philippe Legault, Eugene Sha, Aaron G. Graham, Jean Wu, Matthew J. P. Walker, Hanqing Zeng, Panagiotis Patros, Jason Luu, Kenneth B. Kent and Vaughn Betz

2019

FINN-R: An End-to-End Deep-Learning Framework for Fast Exploration of Quantized Neural Networks
Michaela Blott, Thomas B. Preußer, Nicholas J. Fraser, Giulio Gambardella, Kenneth O'brien, Yaman Umuroglu, Miriam Leeser, Kees Vissers

2018

General-Purpose Computing with Soft GPUs on FPGAs

Muhammed Al Kadi, Benedikt Janssen, Jones Yudi and Michael Huebner

2017

Hoplite: A Deflection-Routed Directional Torus NoC for FPGAs

Nachiket Kapre and Jan Gray

2016

Fine Grained Interconnect Synthesis

Alex Rodionov, David Biancolin and Jonathn Rose.

2015

The Cibola Flight Experiment

Heather Quinn, Diane Roussel-Dupre, Mike Caffrey, Paul Graham, Michael Wirthlin, Keith Morgan, Anthony Salazar, Tony Nelson, Will Howes, Eric Johnson, Jon Johnson, Brian Pratt, Nathan Rollins and Jim Krone

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