About the Technical Committee

The ACM/SIGDA Technical Committee on FPGAs and Reconfigurable Computing is an ACM SIGDA (Special Interest Group for Design Automation) Technical Committee formed to promote the FPGA and Reconfigurable Computing Community. This includes:

- Promoting Research and Researchers in this community to the larger ACM and technical world.
- Providing guidance for new reseachers entering the community.
- Facilitating community self-governance, including identifying directions for the field.

Members

- Vaughn Betz, University of Toronto (Member-at-Large)
- Michaela Blott, Xilinx
- Deming Chen, University of Illinois at Urbana-Champaign
- André DeHon, University of Pennsylvania (Chair)
- Suhaib Fahmy, KAUST
- Farinaz Koushanfar, University of California at San Diego
- Brad Hutchings, Brigham Young University (Secretary/Treasurer)
- Michael Hutton, Google
- Paolo Ienne, EPFL
- Miriam Leeser, Northeastern University
- Philip Leong, University of Sydney (Member-at-Large)
- Jing Li, University of Pennsylvania
- Wayne Luk, Imperial College (Member-at-Large)
- Andrew Putnam, Microsoft
- Shepard Siegel, Atomic Rules
- Steve Trimberger, Xilinx (Past Chair)
- Steve Wilton, University of British Columbia
- Wang Yu, Tsinghua University
- Grace Zgheib, Intel

Revision #3
Created 2024-09-07 13:47:07 UTC by Suhaib Fahmy (Admin)
Updated 2024-09-21 09:54:02 UTC by Suhaib Fahmy (Admin)